

Applicant: Keizo Takechi, et al.

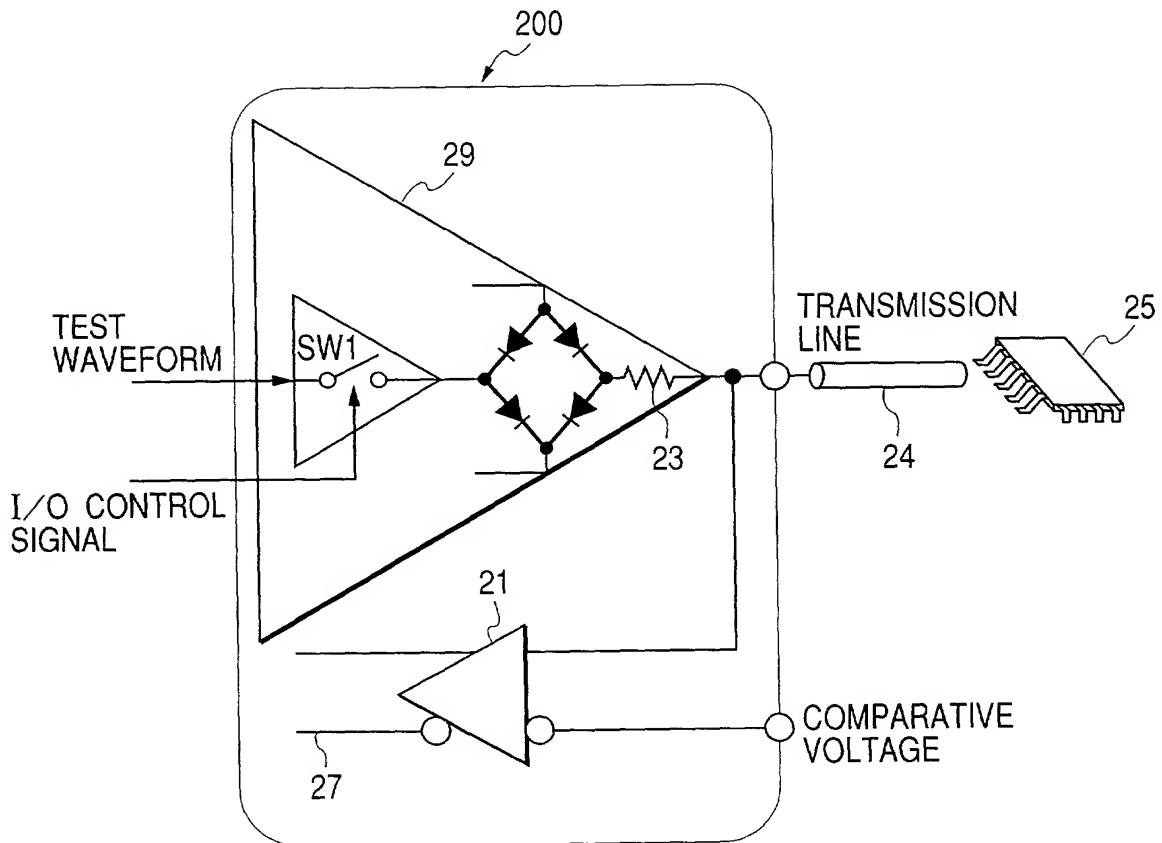
Title: Driver Circuit Integrated with Load Current Output Circuit, Pin...

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FIG. 1



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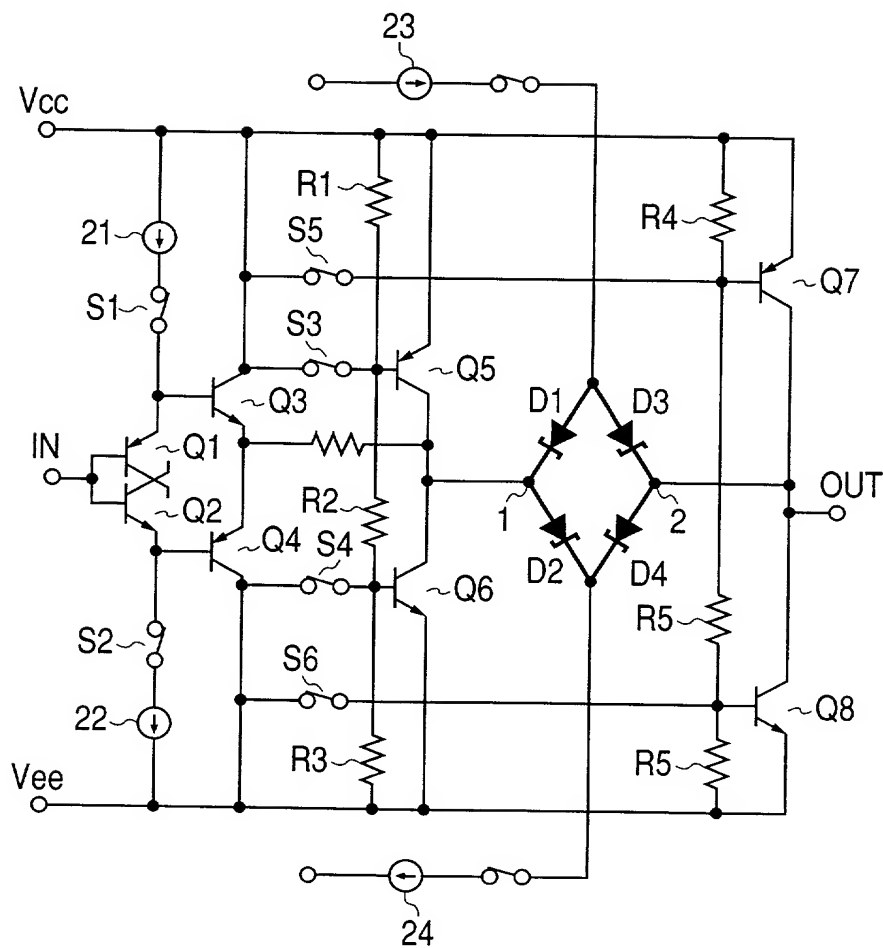
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FIG. 2



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FIG. 3A

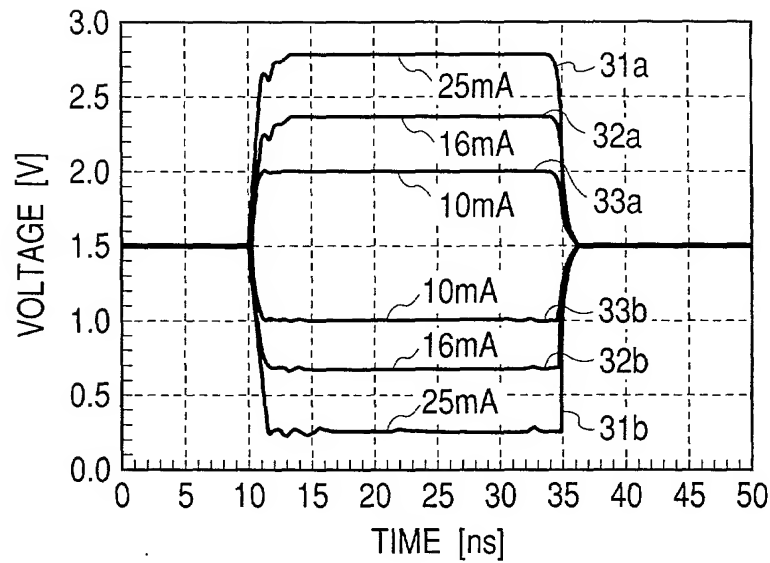
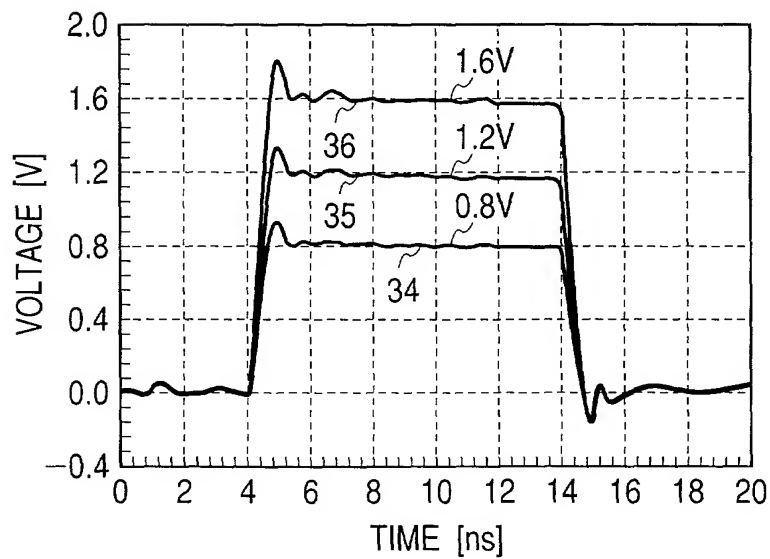


FIG. 3B



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FIG. 4A

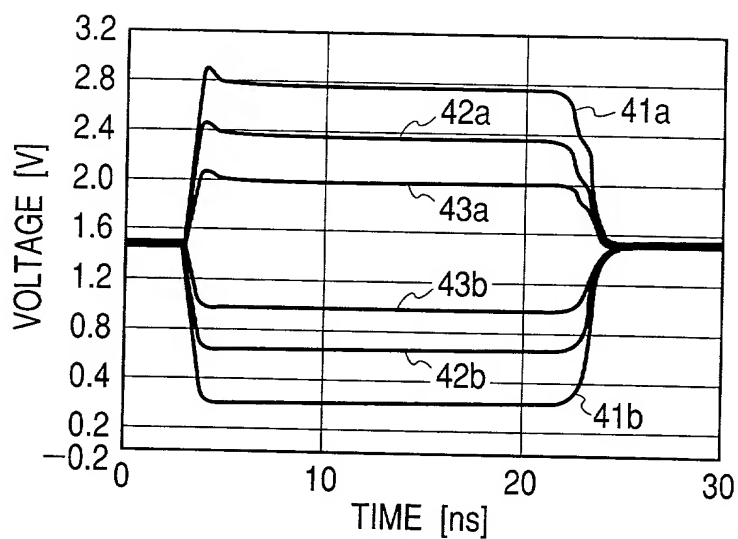
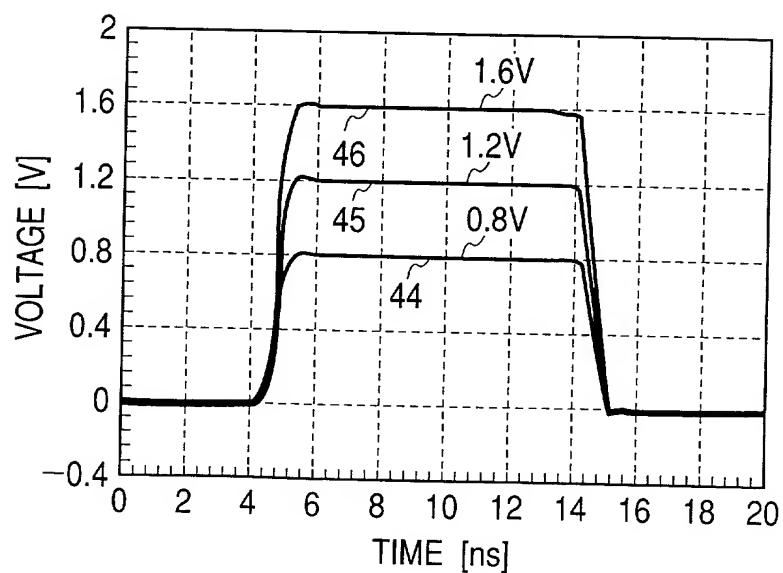


FIG. 4B



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FIG. 5

ITEMS	INTEGRAL-TYPE DRIVER	CONVENTIONAL	
		DRIVER	DYNAMIC LOAD
ELECTRICITY CONSUMPTION	4.5W	3.6W 7.1W	3.5W
CHIP AREA [mm ²]	4.0×4.0 =16.0	3.7×3.7 =22.69	3.0×3.0
MOUNTING AREA [mm ²]	460	690	

FIG. 6

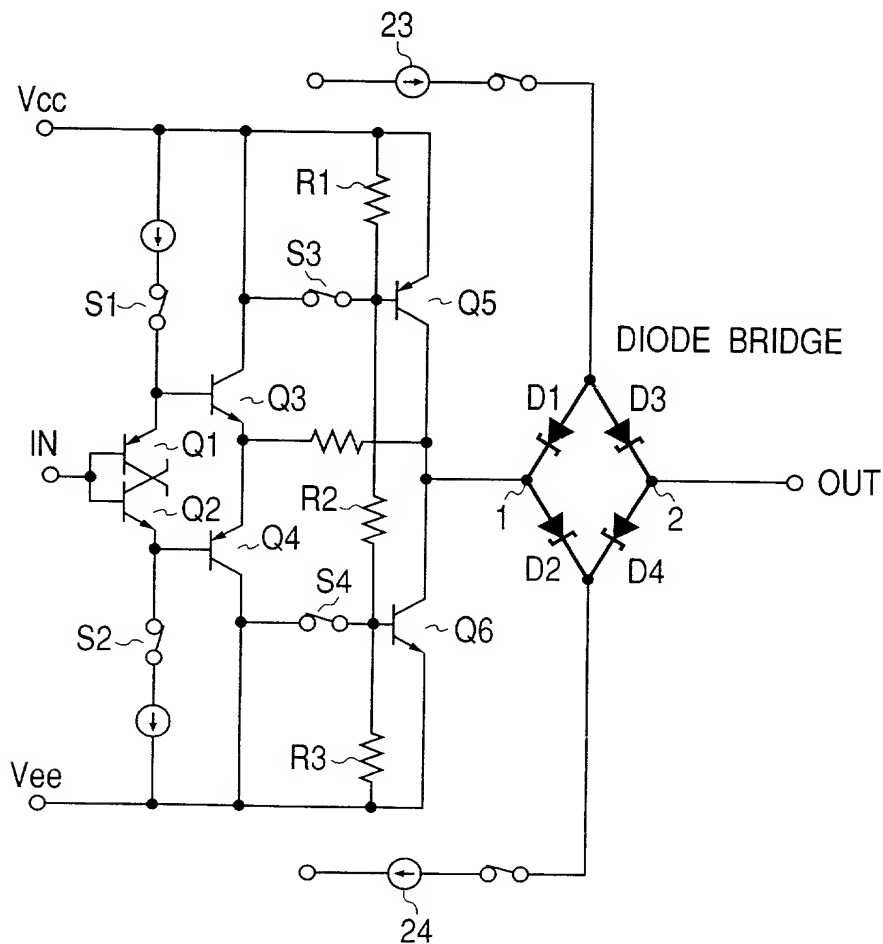


FIG. 7

ITEMS	CIRCUIT SYSTEM OF FIG. 2	CIRCUIT SYSTEM OF FIG. 3
ELECTRICITY CONSUMPTION	700mW	1500mW
CHIP AREA [mm ²]	0.7 (CONVENTIONALLY 1.0)	0.7 (CONVENTIONALLY 1.0)
RISING TIME (1.6V)	200ps	300ps

FIG. 8

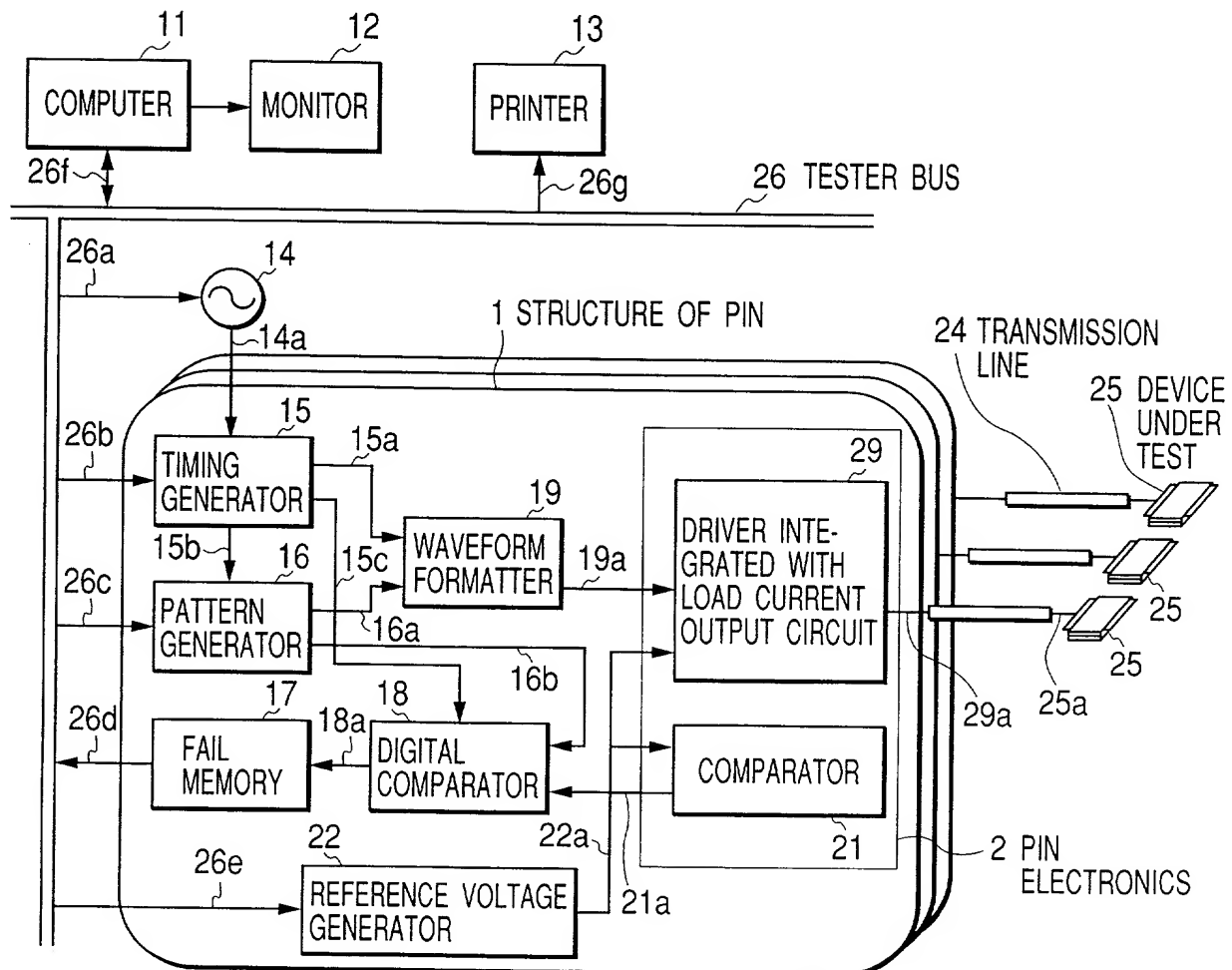
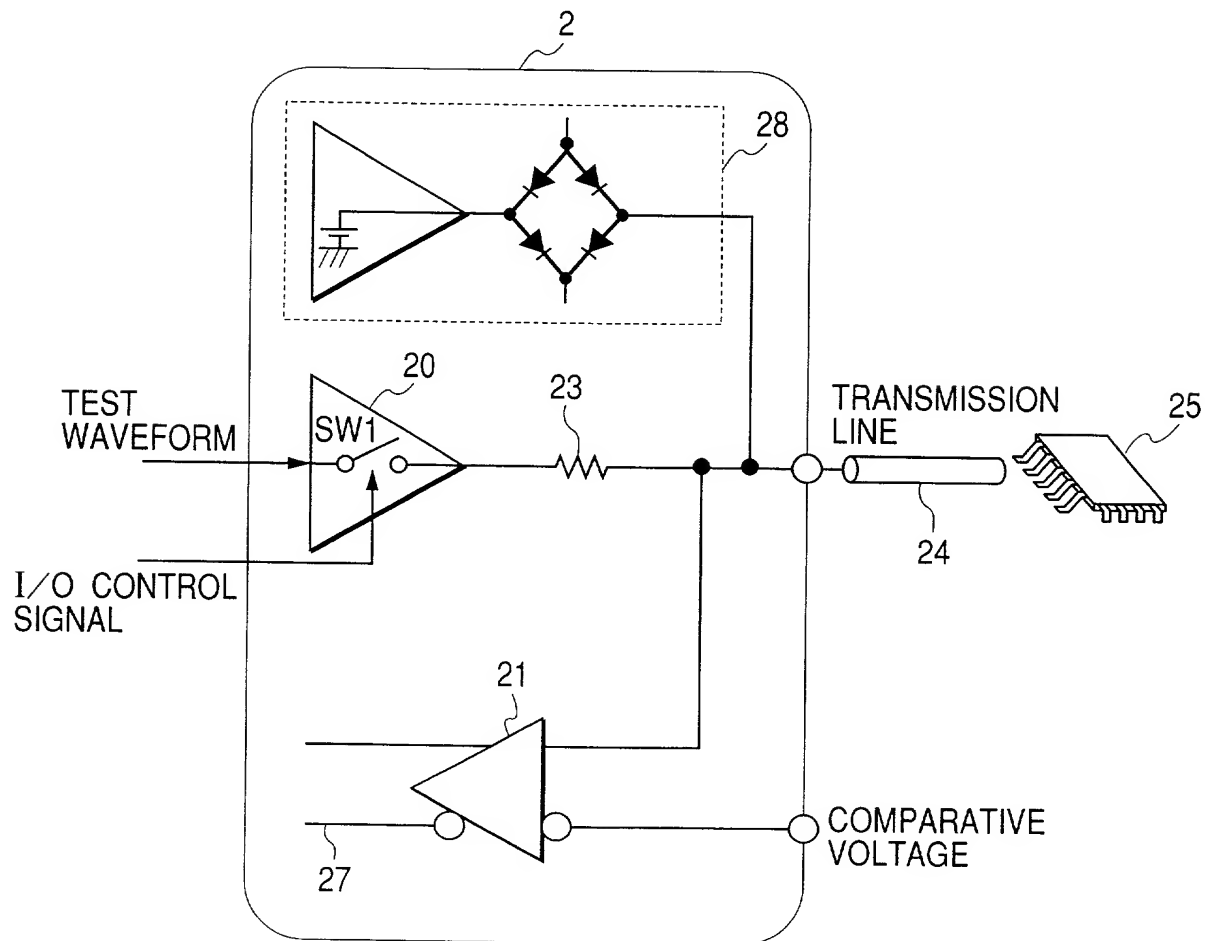


FIG. 9 PRIOR ART



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FIG. 10
PRIOR ART

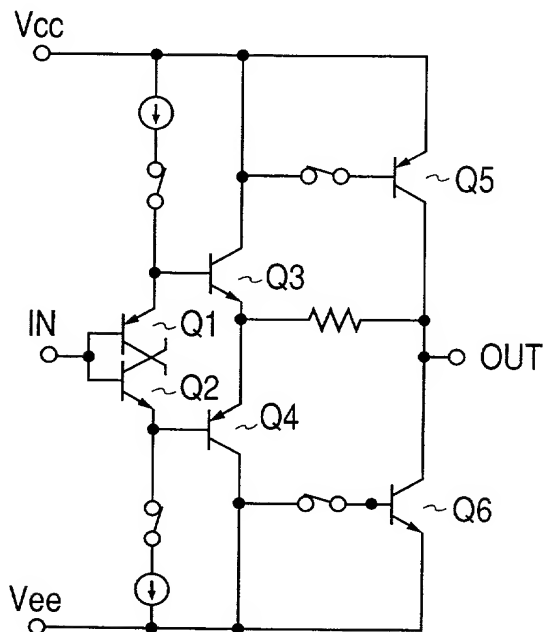


FIG. 11
PRIOR ART

